



LC2129

3A 1.5MHz 5.5V Synchronous Buck Converter

DESCRIPTION

The LC2129 is a high efficiency synchronous, buck DC/DC converter. Its input voltage range is from 2.6V to 6V and provides an adjustable regulated output voltage from 0.8V to 5.5V while delivering up to 3A of output current.

The internal synchronous switches increase efficiency and eliminate the need for an external Schottky diode. The switching frequency is set by an external resistor or can be synchronized to an external clock. The 100% duty cycle provides low dropout operation extending battery life in portable systems.

The LC2129 is operated in PFM/PWM auto-switch mode which enhance the efficiency at light-load.

The LC2129 is available in the DFN2x2-8L and DFN3x3-10L package

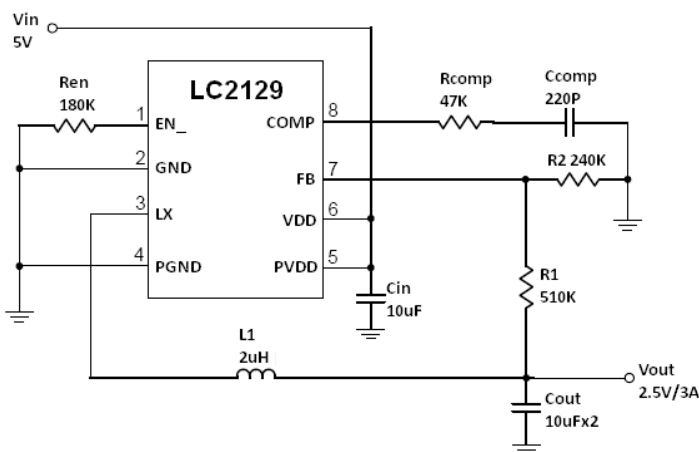
FEATURES

- Adjustable Output Voltage, $V_{fb}=0.8V$
- Maximum output current is 3A
- Range of operation input voltage: Max 6V
- Standby current: 0.5mA (typ.)
- Line regulation: 0.1%/V (typ.)
- Load regulation: 10mV (typ.)
- High efficiency, up to 96%
- Environment Temperature: $-20^{\circ}C \sim 85^{\circ}C$

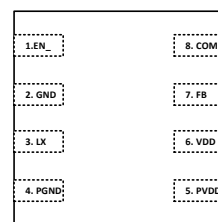
APPLICATIONS

- Power Management for 3G modem
- 3W LED driver from Li-ion battery
- LCD Monitor and LCD TV
- DVD Decode Board
- ADSL Modem
- Post Regulators for Switching Supplies

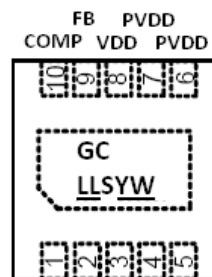
TYPICAL APPLICATION



PIN OUT & MARKING



DFN2x2-8L



DFN3x3-10

ABSOLUTE MAXIMUM RATING

Parameter	Value
Max Input Voltage	6V
Max Operating Junction Temperature(Tj)	125°C
Ambient Temperature(Ta)	-20°C – 85°C
Package Thermal Resistance	DFN2x2-8L 25°C / W
Storage Temperature(Ts)	-40°C - 150°C
Lead Temperature & Time	260°C, 10S
ESD (HBM)	>2000V

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

RECOMMENDED WORK CONDITIONS

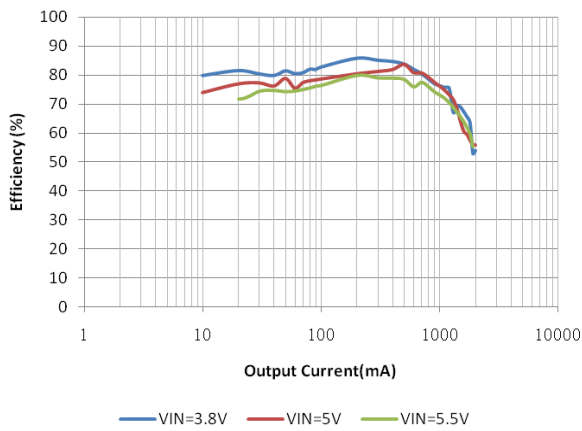
Parameter	Value
Input Voltage Range	Max. 6V
Operating Junction Temperature(Tj)	-20°C –125°C

ELECTRICAL CHARACTERISTICS

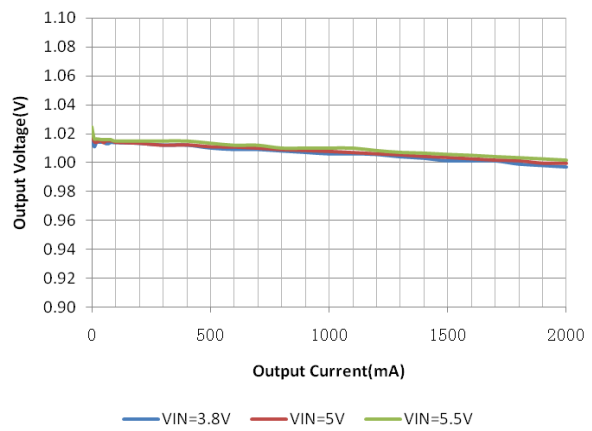
(VDD=5V, TA=25°C)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
VDD	Input Voltage Range		2.6		5.5	V
Vref	Feedback Voltage		0.784	0.8	0.816	V
I _{fb}	Feedback Leakage current			0.1	0.4	uA
I _q	Quiescent Current	Active, V _{fb} =0.78, No Switching		450		uA
		Shutdown		1		uA
LnReg	Line Regulation	V _{in} =4V to 5.5V		0.1		%/V
LdReg	Load Regulation	I _{out} =1 to 3A		0.02		%/A
G _m	EA Transconductance			600		us
F _{soc}	Switching Frequency	R _{en_} =180K		1.35		MHz
R _{dsonP}	PMOS R _{dson}			150		mohm
R _{dsonN}	NMOS R _{dson}			130		mohm
I _{limit}	Peak Current Limit			3.8		A
V _{en_}	EN_ Shutdown Voltage		V _{in} -0.7V		V _{in}	

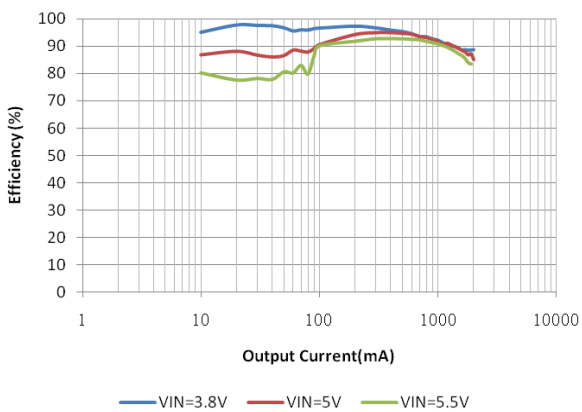
Efficiency vs. Output Current
(VOUT=1.0V)



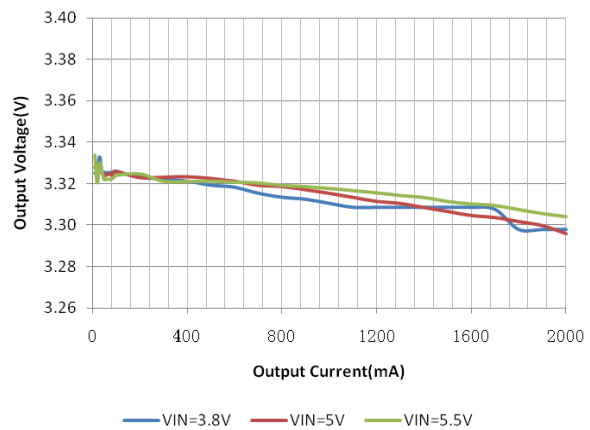
Load Regulation
(VOUT=1.0V)



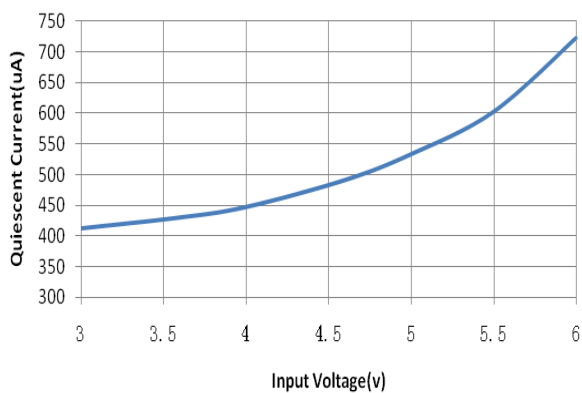
Efficiency vs. Output Current
(VOUT=3.3V)



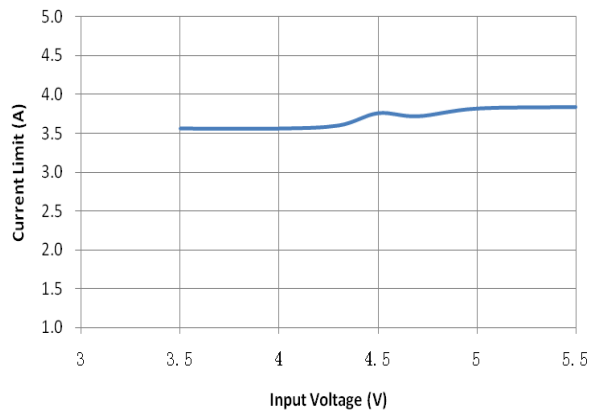
Load Regulation
(VOUT=3.3V)



Quiescent Current vs. Input Voltage

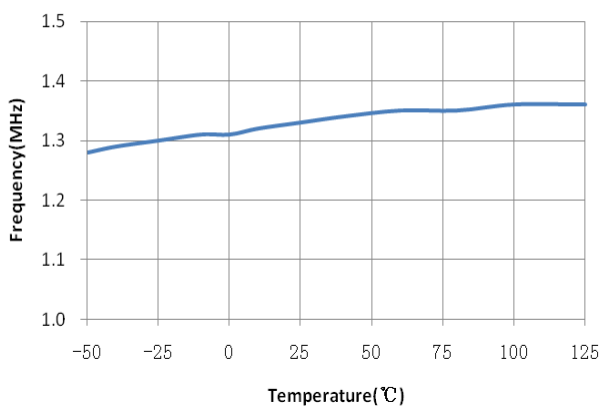


Current Limit vs. Input Voltage



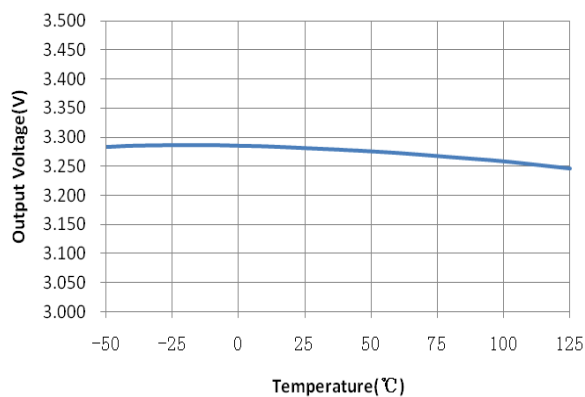
Frequency vs. Temperature

(VIN=5V)

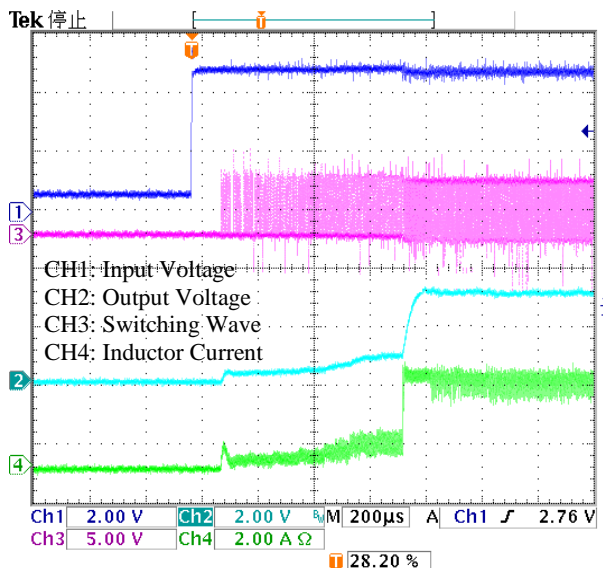


Output Voltage vs. Temperature

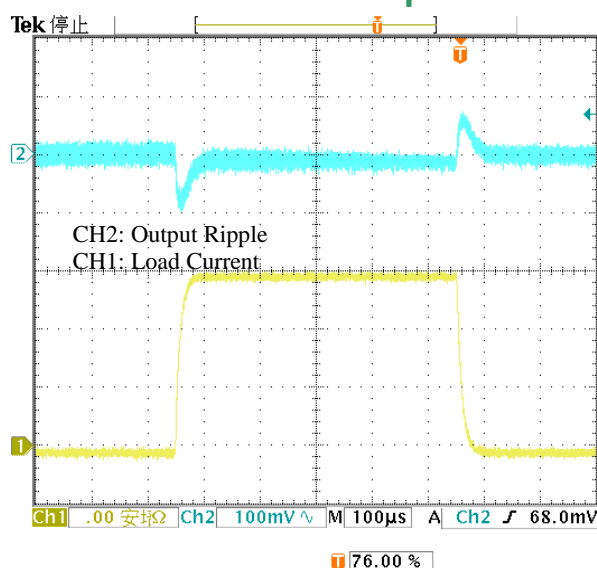
(VIN=5V)



Power On & Soft Start



Load Transient Response



DETAILED DESCRIPTION

LC2129 is a 3A synchronous buck, with frequency adjusted by Ren_. It can achieve conversion efficiency up to 95%. It also support 100% duty cycle which will maximize the battery usage. Only a inductor and a few R & C need for peripheral. The PCB size can be very small

Please note that EN_ pin has to be pull high if one wants to shutdown the chip. And release it (with a Ren_ connected to GND) to have it work. One can also switch off LC2129 by connect enable signal with at least 1mA driving capability to VDD pin (pin 6 of DFN2x2 and pin 8 of DFN3x3 package).

PACKAGE OUTLINE

Package	DFN2x2-8L	Devices per reel		Unit	mm																																																				
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